



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yunlong Sun and Edward J. Swenson

Group Art Unit: 2106

Application No. 08/538,073

Filed: October 2, 1995

For: **LASER SYSTEM AND METHOD FOR FUNCTIONAL
TRIMMING OF FILMS AND DEVICES**

Date: November 10, 1997

Examiner: Geoffrey S. Evans

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GROUP 2100

TRANSMITTAL LETTER

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith in connection with the above-identified application are
the following documents:

1. ☒ Information Disclosure Statement.
2. ☒ Form PTO-1449.
3. ☒ Copies of cited references (33).
4. ☒ The Commissioner is hereby authorized to charge any additional
fees which may be required in connection with the filing of the
enclosed documents or to credit any overpayment to Deposit
Account No. 19-4455. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Yunlong Sun and Edward J. Swenson

By *Michael L. Levine*
Michael L. Levine
Registration No. 33,947

STOEL RIVES LLP
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Facsimile: (503) 220-2480
Attorney Docket No. 26860/33:43 US



GP 2106

Application No.: 08/538,073

Filed: October 2, 1995

Inventor(s): Yunlong Sun and Edward J. Swenson

Title: LASER SYSTEM AND METHOD FOR FUNCTIONAL TRIMMING OF FILMS AND DEVICES

Art Unit: 2106

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Attorney Docket No.: 26860/33:43 US

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CERTIFICATE OF MAILINGDate of Deposit November 10, 1997

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS
BEING DEPOSITED WITH THE UNITED STATES POSTAL
SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE
ADDRESSED TO: ASSISTANT COMMISSIONER FOR
PATENTS, WASHINGTON, D.C. 20231

Signature

Deanna J. SullivanTyped or printed name of person signing
certificate

Attachment:

- ☒ Information Disclosure Statement
- ☒ Form PTO-1449
- ☒ Cited References



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In re application of

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Am
1-2-97

INFORMATION DISCLOSURE STATEMENT

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Pursuant to their duty of disclosure, applicants enclose copies of the references listed on the accompanying Form PTO-1449. These references were brought to the attention of applicants' agent after payment of the issue fee. Applicants request, therefore, that the enclosed documents be placed in the file in accordance with 37 CFR 1.97 (i). Applicants' agent believes that these references are merely cumulative to the art already cited during prosecution.

Respectfully submitted,

Yunlong Sun and Edward J. Swenson

By Michael L. Levine
Michael L. Levine
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Order

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
26860/33:43APPLICATION NO.
08/538,073INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)APPLICANT
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5 0 9 1 9 9 1	02/25/92	Tulip et al.	372	4	
	AB	4 9 6 5 8 0 3	10/23/90	Esterowitz et al.	372	5	
	AC	4 9 4 2 5 8 2	07/17/90	Kintz et al.	372	18	
	AD	4 9 3 0 9 0 1	06/05/90	Johnson et al.	372	26	
	AE	4 7 3 1 7 9 5	03/15/88	Clark et al.	372	107	
	AF	4 6 5 6 6 3 5	04/07/87	Baer et al.	372	27	
	AG	4 1 7 2 7 4 1	10/30/79	Johnson	148	1.5	
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AJ							
	AK							
	AL							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AM	1995 "Optimization of Memory Redundancy Laser Link Processing," Sun et al.
AN	1991 "Focused Ion Beams," Orloff
AO	1990 "Polysilicon Resistor Trimming by Laser Link Making," Parker, et al.
AP	1989 "Polycrystalline Silicon Resistor Trimming by Laser Annealing," Crowley
AQ	1989 "Explosion of Poly-Silicide Links in Laser Programmable Redundancy for VLSI Memory Repair," Lu, Chlipala, Scarfone

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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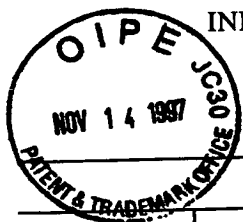
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Page)

AA	1989 "Computer-Simulated Explosion of Poly-Silicide Links in Laser-Programmable Redundancy for VLSI Memory Repair," Chlipala, Scarfone
AB	1987 "Laser Processing for Application Specific Integrated Circuits (ASICs)," Smart & Stewart
AC	1986 "Computer Simulation of Target Link Explosion in Laser Programmable Redundancy for Si Memory," Scarfone & Chlipala
AD	1984 "Considerations of Laser Material Interactions for the Trimming of Thin Film Resistors," Smart
AE	1983 "Laser Adjustment of Linear Monolithic Circuits," Litwin & Smart
AF	1982 "Semiconductor Processing Applications with Lasers," Kestenbaum
AG	1982 "Laser-Assisted Poly-Si Resistor Fuse Utilizing Silicon Lateral Flow," Iwai, et al.
AH	1981 "Laser Programmable Redundancy and Yield Improvement in a 64k DRAM," Smith et al.
AI	1981 "Laser Beam Processing," Cable
AJ	1981 "A Technique for Automatic Focusing and Alignment of Monolithic Circuits for Laser Trimming," Wells et al.
AK	1980 "Photoexcitation Effects During Laser Trimming of Thin-Film Resistors on Si," Kestenbaum & Baer
AL	1980 "Lasers in Semiconductor Processing," Burggraaf

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AA

1979 "Reliability of LSI Memory Circuits Exposed to Laser Cutting," Ram

AB

1979 "Interference Effects in Laser Micromachining of Thin Films on Silicon," Kestenbaum

AC

1979 "A Fault-Tolerant 64k Dynamic Ram," Cenker, et al.

AD

1978 "Effect of Vapor Plasma on the Coupling of Laser Radiation with Al Targets," Shui, et al.

AE

1977 "Laser Vaporization of Metal Films - Effect of Optical Interference in Underlying Dielectric Layers,"
North

AF

1976 "A Passive Laser-Trimming Technique to Improve D/A Linearity," Price

AG

1975 "Connections and Disconnections on Integrated Circuits Using Nanosecond Laser Pulses," Cook, et al.

AH

1974 "An Overview of Laser Functional Trimming Techniques," Hurtig & Swenson (ESI)

AI

1974 - "Algorithmic Trimming on Active Circuitry," Faber

AJ

AK

AL

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